```
Controlling a program
// child
close(STDIN FILENO)
dup2(indes[0], STDIN FILENO)
close(indes[0])
clsse(indes[1])
                          // outdes
execlp("./circle", "./circle", (char *)NULL)
fcntl(indes[1], F SETFL, fcntl(indes[1], F GETFL) | O NONBLOCK) // outdes
do{
   nread = read(outdes[0], buffer, MAX BUFFER);
while(nread <= 0);</pre>
write(STDOUT FILENO, buffer, nread);
Initialize Device
if(!use mem) {
   if(! request_region(short_base, NR, "short"))
       ERROR
}else{
   if(! request mem region(...))
       ERROR
    short base = (unsigned long) ioremap(short base, NR)
}
result = register_chrdev(major, "short", &short fops);
if(result < 0)
  ERROR - release_?_region
if(scull major)
   dev = MKDEV(scull_ajor, scull_minor)
   result = register_chrdev_region(dev, NR, "scull")
else
    result = alloc chrdev region(&dev, scull minor, NR, "scull")
    scull major = MAJOR(dev)
scull devices = kmalloc(NR*sizeof(struct scull_dev), GFP_KERNEL)
memset(scull devices, 0, size)
for (i \rightarrow NR)
  initialize
_____
Self Probe Example
** mask = probe irq on()
short irq = 0
outb p(0x10, short base+2)
outb_p(0x00, short_base)
outb_p(0xFF, short_base)
outb p(0x00, short base+2)
udelay(5)
** short irq = probe irq off(mask)
                                 _____
Pipe içinde yapılanlar - bekletmeler
while(1){
    inpipe = pipe2[0]
                               // outpipe
    FD ZERO(&fds rd)
    FD_SET(inpipe, &fds_rd)
                               // outpipe
    select(n+1, &fds rd, &fds wr, NULL, NULL)
    if (FD ISSET (inpipe, &fds rd))
        \overline{i} = \text{read(inpipe, buf, 1024)}
______
IRQ ne zaman register edilmeli
                                    First Opening > Registering Device
```

The interrupt handler can be installed either at driver initialization or when the device is first opened. Although installing the interrupt handler from within the module's initialization function might sound like a good idea, it often isn't, especially if your device does not share interrupts. Because the number of interrupt lines is limited, you don't want to waste them. You can easily end up with more devices in your computer than there are interrupts. If a module requests an IRQ at initialization, it prevents any other driver from using the interrupt, even if the device holding it is

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never used. Requesting the interrupt at device open, on the other hand, allows some
sharing of resources.
IRQ autodetect - initialize, probe handling, open, close
One of the most challenging problems for a driver at initialization time can be how
to determine which IRQ line is going to be used by the device. Autodetect
if (short irq < 0) /* not yet specified: force the default on */
  switch(short_base) {
    case 0x378: short_irq = 7; break;
    case 0x278: short_irq = 2; break;
    case 0x3bc: short irq = 5; break;
// interrupt handler
irqreturn t short interrupt(int irq, void *dev id, struct pt regs *regs){
     struct timeval tv
     int written
     do gettimeofday(&tv)
     \overline{\text{written}} = \text{sprintf((char*)short\_head, "\$08u.\$06u\n", (int)(tv.tv sec \$ 100000000),}
(int)(tv.tv usec))
     short incr bp(&short head, written)
     wake_up_interruptable(&short_queue)
     return IRQ_HANDLED
int short open(struct inode *inode, struct file *filp)
    extern struct file_operations short_i_fops
    if(iminor(inode) & 0x809
       filp->f op = &short i fops
    return 0
irqreturn_t short_probing(int irq, void *dev_id, struct pt_regs *regs){
     if(short irq == 0)
         short irq = irq;
     if(short_irq != irq)
         short_irq = -irq;
     return IRQ HANDLED
}
Autodeteched irq İnterruptor yaz
if(short irq >= 0)
    result = request_irq(short_irq, short_interrupt,
            SA INTERRUPT, "short", NULL)
    outb(0x10, \frac{-}{}short_base + 2)
One of the main problems with interrupt handling is how to perform lengthy tasks
within a handler. Often a substantial amount of workmust be done in response to a
device interrupt, but interrupt handlers need to finish up quickly and not keep interrupts
blocked for long.
Linux (along with many other systems) resolves this problem by splitting the interrupt
handler into two halves. The so-called top half is the routine that actually
responds to the interrupt—the one you register with request irq. The bottom half is a
routine that is scheduled by the top half to be executed later, at a safer time.
I/O System layerlerini açıkla
user processes
device-independent software
device drivers
interrupt handlers
hardware
           _____
I/O Request lifecycleyi açıkla
request I/O (user process) -> system call
can already satisfy request? -> no
send request to device driver -> process request
device controller -> interrupt -> I/O completed
interrupt handler -> receives interrupt
store data in buffer
determine which I/O completed -> transfer data
I/O completed
Advantages = Device Drivers as Modules
provides a standart interface
hides details of the hardware devices communication protocols
```